

## EXHIBIT B

### Assignment of Patent Rights

Whereas, AirIP Corporation, a Delaware corporation, with an office at 2100 Geng Road, Suite 200, Palo Alto, CA 94303 (hereinafter COMPANY) is the sole and exclusive owner of certain United States and/or foreign patents and/or patent applications listed in Exhibit A annexed hereto (collectively referred to as the "Patents"); and

Whereas Gallitzin Allegheny LLC, with an office at 171 Main Street, #271, Los Altos CA 94022, (hereinafter ASSIGNEE) is desirous of acquiring the right, title and interest in, to and under the said Patents (and all foreign counterparts and related foreign patents).

#### *Now, Therefore,*

For good and valuable consideration, the receipt of which is hereby acknowledged, COMPANY does hereby sell, assign, transfer and set over to ASSIGNEE, the Patents aforesaid, and any inventions claimed in said Patent, any reissue or reissues of said Patents already granted and which may be granted, any certificates of reexamination already granted and which may be granted the same to be held and enjoyed by ASSIGNEE for its own use and enjoyment, all extensions, continuations, continuations in part, continuing prosecution applications, and divisions of such patents and applications; provisional patent applications that are or will be continuations or continuations in part of such patents and applications; and foreign counterparts to any of the foregoing including, without limitation, utility models, and for the use and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Patents are or may be granted, reissued or extended as fully and entirely as the same would have been held and enjoyed by COMPANY, if this assignment and sale had not been made; together with all claims for damages by reason of past infringement of said Patents, with the right to sue for, and collect the same for its own use and behalf, and for the use and behalf of its successors, assigns or other legal representatives.

And, COMPANY, hereby authorizes and requests the Commissioner of Patents and Trademarks to issue any and all Letters Patents of the United States on said inventions to ASSIGNEE as assignee of the entire interest, and hereby covenants that COMPANY has full

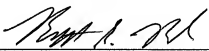
right to convey the entire interest herein assigned, and that, except as otherwise provided between the parties, COMPANY has not executed, and will not execute, any agreements in conflict therewith.

In Witness Whereof, the parties, by their duly authorized representatives, have executed this Assignment.

DATE: Aug 30, 2004

By: BENNETT S. DUBIN  
Printed/Typed Name

Title: CEO

  
Signature

**CALIFORNIA ALL-PURPOSE ACKNOWLEDGMENT**

State of California

County of Santa Clara } ss.On August 30, 2004 before me, Katherine N. Wang Notary Public  
Date Name and Title of Officer (e.g., "Jane Doe, Notary Public")  
personally appeared Bennett S. Dubin  
Name(s) of Signer(s)☒ personally known to me  
☐ proved to me on the basis of satisfactory evidence

to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.



WITNESS my hand and official seal.

  
Signature of Notary Public**OPTIONAL**

*Though the information below is not required by law, it may prove valuable to persons relying on the document and could prevent fraudulent removal and reattachment of this form to another document.*

**Description of Attached Document**Title or Type of Document: Patent Purchase Agreement Exhibit B  
Document Date: August 11, 2004 Number of Pages: 15  
Signer(s) Other Than Named Above: \_\_\_\_\_**Capacity(ies) Claimed by Signer**Signer's Name: Bennett S. Dubin

- ☐ Individual  
☒ Corporate Officer — Title(s): CEO  
☐ Partner — ☐ Limited ☐ General  
☐ Attorney-in-Fact  
☐ Trustee  
☐ Guardian or Conservator  
☐ Other: \_\_\_\_\_

Signer Is Representing: Air IP Corporation**RIGHT THUMBPRINT  
OF SIGNER**  
Top of thumb here

# EXHIBIT A

## Transferred Patents and Patent Applications

Patent # App #	AirP	Filing Date	Publish Date	Title	Area
ISSUED 5943216 24 Aug 1999	000	6/3/1997	8/24/1999	Apparatus for providing a two-sided, cavity, inverted-mounted component circuit board	Semiconductor;miniaturization
ISSUED 6278481 21 Aug 2001	001	10/3/1996	8/21/2001	Photocard that is inserted into a non-digital camera to enable the non-digital camera to take digital photographic images	Imaging
ISSUED 6452088 17 Sep 2002	004	4/16/2001	9/17/2002	Power generating display	Power Management
ISSUED 6707115 26 Feb 2004	007	4/16/2001	10/17/2002	Transistor with minimal hot electron injection	RF: semiconductor
ISSUED 6710424 4 Mar 2004	017	9/21/2001	3/27/2003	Radio frequency chipset architecture	RF: architecture
ISSUED 6713839 11 Mar 2004	022	5/24/2002	11/27/2003	Programmable latch using antifuses	Programmable;semiconductor & circuits
ALLOWED 09/887905 20030014682	009	6/22/2001	1/16/2003	Clock generation systems and methods	Baseband: clocking & architecture

ALLOWED 09/887908 20020196029	010	6/22/2001	12/26/2002	Systems and methods for testing wireless devices	Baseband: testing
ALLOWED 09/930828 20030034851	011	8/15/2001	2/20/2003	Systems and methods for self-calibration	Baseband: clocking
ALLOWED 10/155508 20030218487	023	5/24/2002	11/27/2003	Programmable latch array using antifuses	Programmable: semiconductor & circuits
09/802,467 20040141088	002	3/9/2001	7/22/2004	Systems and methods to reversibly convert a film-based camera into a digital camera	Imaging
09/802,411 20020128037	003	3/9/2001	9/12/2002	Single chip wireless communication integrated circuit	Baseband: architecture
09/814,355 20020169990 (Now split into 2 patent applications)	005	3/21/2001	11/14/2002	Low power clocking systems and methods	Baseband: clocking
09/837,651 20020184546	006	4/18/2001	12/5/2002	Method and device for modifying the memory contents of and reprogramming a memory	Baseband: clocking

09/887906 20020197998	008	6/22/2001	12/26/2002	Cellular channel bonding for improved data transmission.	Baseband: architecture
09/930,827 20030035388	012	8/15/2001	2/20/2003	RF sniffer	RF: architecture
09/930,822 20030034848	013	8/15/2001	2/20/2003	Ring oscillator dynamic adjustments for auto calibration	Baseband: clocking
09/930,816 20030103147	014	8/15/2001	6/05/2003	Photocard that is inserted into a non-digital camera to enable the non-digital camera to take digital photographic images	Imaging:
09/962,718 20030058830	015	9/21/2001	3/27/2003	Channel interference reduction	Baseband: architecture
09/962937 20030058057	016	9/21/2001	2/23/2003	Integrated CMOS high precision piezo-electrically driven clock	Baseband: clocking
09/973608 20030080792	018	10/9/2001	5/1/2003	Systems and methods for minimizing harmonic interference	Baseband: clocking

09/973579 20030067894	019	10/9/2001	4/10/2003	Flexible processing system	Baseband: architecture
09/973621 20030067737 (Now split into 2 patent applications)	020	10/9/2001	4/10/2003	On chip capacitor	Semiconductor: miniaturization
10/074629 20030152164	021	2/14/2002	8/14/2003	FFT and FHT engine	Baseband: architecture
10/196968 20040056798	026	7/17/2002	3/25/2004	Local positioning system	Baseband: architecture
10/690263	027	originally 6/5/2002; later refiled		Integrated circuit capable of working with multiple bus interface standard.	Semiconductor: architecture
10/690266	028	originally 6/5/2002; later refiled		Integrated circuit with a plurality of host processor family types	Baseband: architecture
10/690265	029	originally 6/5/2002; later refiled		Wireless security	Baseband: architecture
10/184402 20040142717	030	6/28/2002	7/22/2004	Flexible Multiprocessing System.	Baseband: architecture

10/155485 20030219035	<b>031</b>	5/24/2002	11/27/2003	Dynamically configured antenna for multiple frequencies and bandwidths	RF:architecture
New App	<b>32 (020)</b>	6/2/2004 original 10/9/2001		On chip capacitor	Semiconductor:miniaturization
New App	<b>33 (005)</b>	6/2/2004 original 3/21/2001		Low power clocking systems and methods	Baseband: clocking